



**NATIONAL INSTITUTE OF TECHNOLOGY, ARUNACHAL PRADESH**  
(Established by MHRD, Govt. of India)  
**YUPIA, ARUNACHAL PRADESH -791112, INDIA**

**Dr. Prases K Mohanty**  
**DIC(R & D)**

Website : [www.nitap.ac.in](http://www.nitap.ac.in)  
E-Mail : [deanrnd@nitap.ac.in](mailto:deanrnd@nitap.ac.in)  
Fax No. : 0360- 2284972  
Phone No.: 9485236260

Advt. No.: ECE/P/AM/01/2015/SMDP

**Date: 12-02-2021**

The interview for the temporary post of '**Research Engineer (RE)**' under **SMDP C2SD Project** funded by MEITY, Govt. of India will be conducted in the Dept. of Electronics & Communication Engineering (ECE), NIT Arunachal Pradesh on **22<sup>nd</sup> February 2021 (Monday)** via **GOOGLE MEET** at 12:00 noon onwards.

1	Name of the Post	Research Engineer (RE) - 01
2	Name of the Research Project	SMDP – Chip to System Design
3	Name of the Sponsoring Agency	MEITY (Govt. of India)
4	Tenure of the Project	2015 - 2021
5	Tenure of the Assignment	Initially for 06 months and extendable thereafter till the completion of project subject to the satisfactory performance and/or the availability of fund released by sponsoring agency
6	Job Description	<ul style="list-style-type: none"> <li>• RE is required for IC design (Layout to GDS-II).</li> <li>• The candidate is expected to carry out his/her own research as per the discussion with PI.</li> <li>• Other Lab activities as assigned by PI</li> </ul>
7	Consolidated monthly compensation / Fellowship	<b>Rs. 34,000/-</b> per month + HRA (as per norms)
8	Essential Qualifications and experience	<ul style="list-style-type: none"> <li>• BE (ECE/EE/EIE/EEE/CSE) and ME/M.Tech in VLSI / Microelectronics / Nanotechnology / Electronic System and related areas.</li> <li>• PhD or PhD (thesis submitted) is desirable.</li> <li>• Knowledge in VLSI design tools (Cadence, Mentor Graphics) is a Must. Backend design (transistor-to-chip) exposure is preferred.</li> </ul>
9	Accommodation	Bachelors Accommodation in the Institute Hostel may be provided, subject to availability

Eligible candidates may appear for the online interview at the Department of Electronics & Communication Engineering, National Institute of Technology Arunachal Pradesh. The candidates are advised to send a mail to [majumder.alak@gmail.com](mailto:majumder.alak@gmail.com) on or before **21<sup>st</sup> Feb' 2021 (5.00 PM)** with a subject "**Application for RE position under SMDP project**" along with the relevant documents: CV, mark sheets, research papers (if any) and experience certificate (if any) etc. **attached as a single PDF**. The link of GOOGLE MEET will be shared to the candidates prior to the date of interview. Mere possession of essential/desirable qualification and experience does not guarantee selection for the post. Candidates will be selected on merit and need of the project.

*P. K. Mohanty*  
**DIC(R & D)**

NIT Arunachal Pradesh  
Yupia

Copy to:

1. All Heads of the Departments, NIT Arunachal Pradesh for publication on Departmental Notice Boards, अरुणाचल प्रदेश
2. Dr. Alak Majumder, PI with a request to give wide publicity of the advertisement. National Institute of Technology, Arunachal Pradesh
3. Head of the Department, Electronics & Communication Engineering Yupia, Arunachal Pradesh-791112, India
4. Project file

**डीन (आर एंड डी)/Dean (R & D)**